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**REPLY UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2136**

PATENT
6000-31500/SUN030132

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10,789,311	§	
Filed: February 27, 2004	§	Examiner: Johnson, Carlton
Inventors:	§	Group/Art Unit: 2136
Hans Eberle	§	Atty. Dkt. No: 6000-31500
Nils Gura	§	
Lawrence Spracklen	§	
Sheueling Chang-Shantz	§	
Leonard Rarick	§	
Title: Method and Apparatus for	§	
Implementing Processor	§	
Instructions for	§	
Accelerating Public-Key	§	
Cryptography	§	

RESPONSE TO FINAL ACTION OF NOVEMBER 28, 2007

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Final Action of November 28, 2007, to further highlight why the application is in condition for allowance.